

JPW



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76956

Shinya WATANABE, et al.

Appln. No.: 10/648,276

Group Art Unit: 2811

Confirmation No.: 4435

Examiner: Jennifer M. DOALN

Filed: August 27, 2003

For: SEMICONDUCTOR CHIP HAVING AN ARRAYED WAVEGUIDE GRATING AND
METHOD OF MANUFACTURING THE SEMICONDUCTOR CHIP AND MODULE
CONTAINING THE SEMICONDUCTOR CHIP

AMENDMENT UNDER 37 C.F.R. § 1.111

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 26, 2004, please amend the above-identified application as follows on the accompanying pages.

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